The

S&H Form: (2/01) DOCKET NO. 1687.1005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hiroyuki YAMASHITA, et al.

Serial No: 10/766,955

Group Art Unit: 2825

Confirmation No. 2505

Filed: January 30, 2004

Examiner: Unassigned

For:

HARDWARE/SOFTWARE CO-VERIFICATION METHOD

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

It is requested that the Assignment for Published Patent Application on the Official Filing Receipt be added. The Assignment for Published Patent Application is:

SEMICONDUCTOR TECHNOLOGY ACADEMIC RESEARCH CENTER 17-2, Shin-Yokohama 3-chome Kohoku-ku, Yokohama-shi Kanagawa, JAPAN

as is evidenced by the Assignment, attached to the application as filed. For the convenience of the Patent and Trademark Office, attached is a photocopy of the original receipt on which the errors have been noted in red.

It is requested that a corrected Official Filing Receipt be issued in this application.

Respectfully submitted, STAAS & HALSEY LLP

Date:

By

Registration No. 22,010

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21171 STAAS & HALSEY LLP SUITE 700 1201 NEW YORK AVENUE, N.W. WASHINGTON, DC 20005



CONFIRMATION NO. 2505

UPDATED FILING RECEIPT

OC000000015571418*

Date Mailed: 03/25/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Hiroyuki Yamashita, Yokohama-shi, JAPAN; Takao Shinsha, Yokohama-shi, JAPAN; Hideaki Fujikake, Fukuoka-shi, JAPAN; Toshiaki Kowatari, Tokyo, JAPAN; Tomoya Hirao, Tokyo, JAPAN; Atsushi Ohkuma, Kashiwa-shi, JAPAN; Hiroaki Nishi, Yokohama-shi, JAPAN; Michiaki Muraoka, Tokyo, JAPAN;

Semiconductor Academic Research Center Aggregament for Published Moton 17-2, Shin - Yoko hama 3 Chome Power of Attorney: The patent practitioners associated with Customer Number 21171. Kanagawa, Japan

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2003-024706(PAT. 01/31/2003

If Required, Foreign Filing License Granted: 06/14/2004

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/766,955**

Projected Publication Date: 07/07/2005

Non-Publication Request: No

Early Publication Request: No

Title

Hardware/software co-verification method

Preliminary Class

716

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U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to signed inventor(s) (hereinafter "ASSIGNOR") by

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|----------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------|
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| Address(cs)) | · Japan | • | |
| | | | |
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| Invention) | | | V. C. his standard for Latter Patent |
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| of execution of application, if not concurrent) | (b) filed on January 30, 2004 Serial No. 10 / 766,955 | Washington, DC 2 U.S.A.) is here data, when known. | by authorized to insert in (b) the specified |
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| | IN WITNESS WHEREOF, the | ne undersigned inventor(s) has (have) | affixed his/her/their signature(s). |
| (Signatures) | " Kiroyuki Yaniyishiki Isanaruki Yakaca Shiniha | Hiroyuki Yamashita | July 20, 2004 |
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